

## DAFTAR PUSTAKA

- 1) IEEE std 1159-1995, *IEEE Recommended Practice for Monitoring Electric Power Quality*.
- 2) Dugan, R. C.; & Mc Granaghan, M. F.; & Santoso, S.; & Beaty, H. W. *Electrical Power System Quality 2<sup>nd</sup> Edition*. New York: Mc Graw Hill.
- 3) Kusko, A.; & Thompson, M. T. 2007. *Power Quality in Electrical System*. New York: Mc Graw Hill.
- 4) Anton, D.A.; & Rosyadi, I.; & Ashari, M.; & Suryoatmojo, H. 2009. *Kombinasi Feed Back dan Feed Forward Kontroller PI sebagai Kendali Dynamic Voltage Restorer (DVR) untuk memulihkan Voltage Sag dan Interruption*. Telkomnika, Volume 7, nomor 2. ISSN 1693-6930.
- 5) Sembiring, S.T.; & Gultom, G. 2012. *Analisis Pemulihan Kedip tegangan Akibat Gangguan Satu Fasa ke Tanah dengan Menggunakan Dynamic Voltage Restorer pada Sistem Tiga Fasa dengan Beban Bervariasi*. Visi, Volume 20, nomor 3. ISSN 0853-0203.
- 6) Mohammed, S. A.; & Cerrada, A. G.; & Abdel-Moamen M. A.; & Hasanin, B. 2013. *Dynamic Voltage Restorer (DVR) System for Compensation of Voltage sags, State-of-the-Art Review*. IJCER. Vol. 3. Issue 1. ISSN 2250-3005.
- 7) Radadiya, K. P.; & Vibakhar, C. K.; & Rajani, S. V.; & Bhayani, K. J. 2013. *Voltage Sag/Swell Compensation Using Dynamic Voltage Restorer (DVR)*. IJAIEM. ISSN 2319-4847.
- 8) Patil, S. B.; & Ankaliki, S. G. 2012. *Voltage Sag and Mitigation Using Algorithm for Dynamic Voltage Restorer by PQR Transformation Theory*. International Journal of Engineering Invention. Vol. 1. Issue 5. ISSN 2278-7461.
- 9) Tiwari, H. P.; & Gupta, S. K. 2010. *Dynamic Voltage Restorer Against Voltage Sag*. International Journal of Innovation, Management and Technology. Vol. 1. No. 3. ISSN: 2010-0248.

- 10) Pawar, N. B.; & Kakre, D. M. 2014. *Generation of Different Types of Voltage Sag Using Matlab/Simulink*. IJEIT. Vol. 3. Issue 9. ISSN 2277-3754.
- 11) Kamble, S.; & Thorat, C. 2012. *Characteristics Analysis of Voltage Sag in Distribution System using RMS Voltage Method*. ACEEE International Journal on Electrical and Power Engineering. Vol. 03. No. 01.
- 12) Imran, R. M.; & Srivastava, J. 2014. *Using of Dynamic Voltage Restorer (DVR) to Mitigate Sag & Swell Voltage during Single Line to Ground & Three-Phase Fault*. IJMER. Vol. 4. Issue 2. ISSN: 2249-6645.
- 13) Chaudhary, S. H.; & Gangil, G. M. 2013. *Mitigation of voltage sag/swell using Dynamic Voltage Restorer (DVR)*. IOSR-JEEE. Vol. 8. Issue 4. e-ISSN 2278-1676, p-ISSN 2320-3331.
- 14) El-Shennawy, T. I.; & Mousa, A.; & El-Gammal, M. A.; & Abou-Ghazala, A. Y. 2010. *A Dynamic Voltage Restorer for Voltage Sag Mitigation in a Refinery with Induction Motor Loads*. American Journal of Engineering and Applied Sciences. Vol. 3. No. 1. ISSN 1941-7020.
- 15) Sarvi, M.; & Marefatjou, H. 2013. *Compensation of Voltage Single-phase SAG and SWELL Using Dynamic Voltage Restorer and Different Per-Unit Value Method*. IJECE. Vol. 3. No. 1. ISSN: 2088-8708.
- 16) Garth N., 2006. *Phase Locked Loop Design Fundamental*. Freescale Semiconductor.