

DAFTAR PUSTAKA

- [1] L. Han, J. Koo, D. Pommerenke, D. Beetner, and R. Carlton, "Experimental Investigation of the ESD Sensitivity of an 8-Bit Microcontroller," 2007.
- [2] C. Li, J. Li, J. Wu, and Y. Xiao, "Investigation on the Immunity of Microcontroller to Electrical Fast Transients," pp. 5–8, 2015.
- [3] E. F. Iftene and H. N. L. Teodorescu, "Protecting the code against side attacks using chaotically controlled clock and supply," *2013 Int. Conf. Electron. Comput. Artif. Intell. ECAI 2013*, pp. 4–7, 2013.
- [4] Z. Zheng, Y. Lu, Y. Huang, J. Li, and L. Ren, "The research about peak voltage suppression technique of direct current (DC) torque motor with large inductance that applies the pulse width modulation (PWM) control method," *AUS 2016 - 2016 IEEE/CSAA International Conference on Aircraft Utility Systems*, 2016. .
- [5] I. Sram, "Datasheet Atmega32A," 2014.
- [6] A. Note, Application, "AVR040 : EMC Design Considerations Table of Contents," pp. 1–22, 2016.
- [7] S. W. Vinayak and K. M. Kavitha, "H-Bridge Configuration for Wide Range Speed Control of DC Motor," vol. 5, no. 5, pp. 2013–2016, 2016.
- [8] V. Siliconix, *IRF9540, SiHF9540*, no. V. 2017.
- [9] V. Siliconix, *IRF540, SiHF540*, no. V. 2011.
- [10] Henry W. Ott, *Electromagnetic Compatibility Engineering*. Canada.: simultaneously in Canada., 2009.
- [11] A. M. R. (Ph.D), *Getting EMC Design Right First Time*, A. Eadie (. 2014.

- [12] Atmel, “AVR042 : AVR hardware design considerations,” *Appl. Note*, 2015.
- [13] T. R. Kuphaldt, *Lessons In Electric Circuits, Volume I DC*, Fifth Edit., vol. 1. 2006.
- [14] M. John Bird BSc(Hons), CEng, CSci, CMath, FIET and Fc. FIIE, FIMA, *Electrical Circuit Theory and Technology*, Third edit. Oxford, UK: Elsevier Ltd. All rights reserved, 2007.